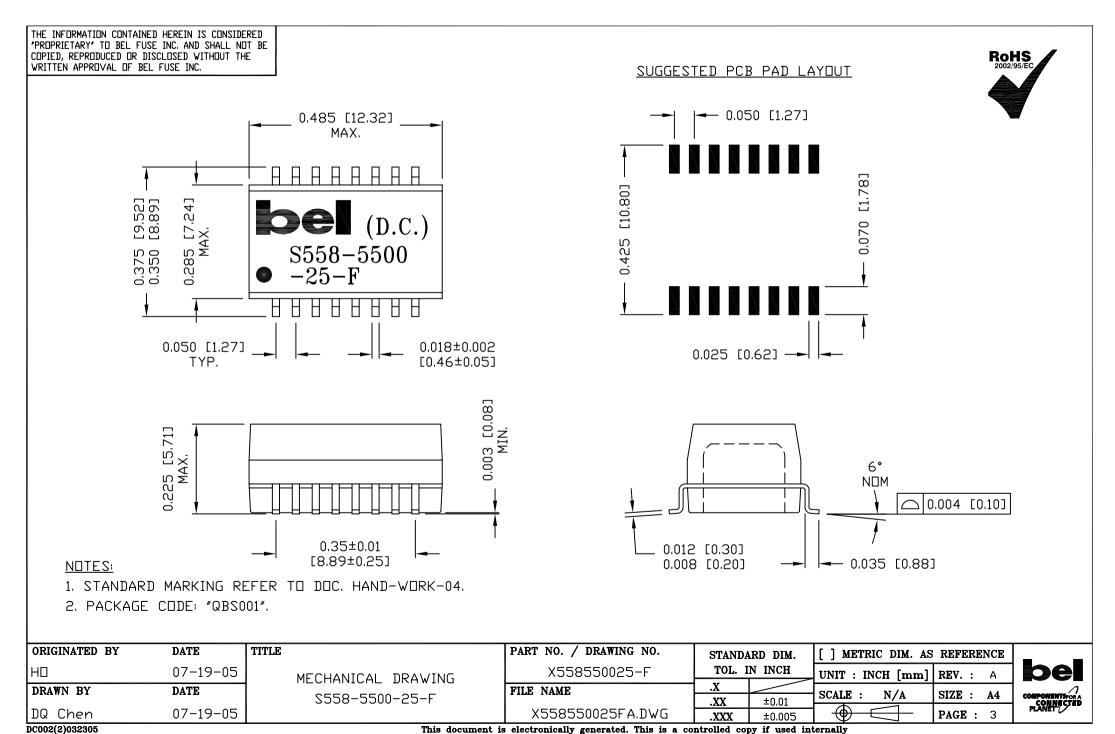
| THE INFORMATION CONTAINED HEREIN IS CONSIDERED 'PROPRIETARY' TO BEL FUSE INC. AND SHALL NOT BE COPIED, REPRODUCED OR DISCLOSED WITHOUT THE WRITTEN APPROVAL OF BEL FUSE INC. | Designed to accommodate 350m DC as stipulated in IEEE 802.3af | | TERISTICS @ 25°C Rohs |
|---|--|--|--|
| SCHEMA | TIC | PIN 1-2-3 : 16-15-14 PIN 6-7-8 : 11-10-9 | 1CT : 1CT 1CT : 1CT |
| CHIP SIDE 1CT : 1CT TD+ 1 | LINE SIDE | DCL (0°~70°C) T× PIN 1-3 DPEN 16-14 R× PIN 6-8 DPEN 11-9 LEAKAGE INDUCTANCE, L1 | 350 µH MIN. @ 100kHz, 100m∨rms WITH 8mA DC BIAS 0.30 µH MAX. |
| | | T× PIN 1-3 SHORT 16-14 R× PIN 6-8 SHORT 11-9 INTERWINDING CAPACITANCE, Cw/w | @ 1MHz, 20m∨ 30 pF MAX. |
| TD- 3 • | 14 TX- | T× PIN 1-3 TO 16-14 R× PIN 6-8 TO 11-9 DCR | @ 1MHz, 20m∨ |
| 4 • 5 • | •13 | PIN (1-3) ; (6-8) INSERTION LOSS 1 MHz TO 100 MHz | 1.1 Ohm MAX. 1.2 dB MAX. |
| RD+ 6 | | RETURN LOSS 1 MHz - 30 MHz 30 MHz -60 MHz 60 MHz - 80 MHz | 18 dB MIN. 18-20 LOG (f/30MHz) 12 dB MIN. |
| | | COMMON TO DIFFERENTIAL 100 kHz – 100 MHz CROSSTALK | 35 dB MIN. 35 dB MIN. |
| RD- 8 • | 9 RX- | 100 kHz - 100 MHz HIPOT | PER HAND-WORK-03 |
| ORIGINATED BY DATE TIT H□ 07-19-05 DRAWN BY DATE DQ Chen 07-19-05 | TLE ∨DIP INTERFACE MDDULE S558-5500-25-F | PART NO. / DRAWING NO.STANDARD DIM.X558550025-FTOL. IN INCHFILE NAME.XX558550025FA.DWG.XXX | [] METRIC DIM. AS REFERENCE UNIT : INCH [mm] REV. : A SCALE : N/A SIZE : A4 PAGE : 2 |

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